

Application No. 10/715,579
Reply dated 1 November 2006
Reply to Office action mailed 27 October 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

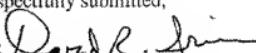
In re: Patent Application of Atsushi Harada
Serial Number: 10/715,579
Filed: 18 November 2003
Title: Block error correction codes with convolutional parity
Examiner: Samir Rizk
Art Unit: 2133
Confirmation No.: 4754
Attorney's Docket: JP920010240US1

Honorable Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

ABANDONMENT

Sir:

Applicant hereby expressly abandons the above-referenced application, Serial 10/715,579.

Respectfully submitted,
By: 

David R. Irvin

Reg. No. 42,682